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GT-I9505 REV0.9(FUSION)

(Main Board / include Sensor HUB)

2013.03.12

sheet01 : GSM / WCDMA / LTE RF
sheet02 : GPS, FM, BT, WIFI, SENSOR HUB, NFC
sheet03 : TRANCEIVER
sheet04 : CP PMIC
sheet05 : CP (MDM9215)
sheet06 : AP (FUSION3)
sheet06 : AP (FUSION3)
sheet08 : AP PMIC
sheet09 : IF PMIC, IRDA, SUB CON, Volume Key
sheet10 : AUDIO
sheet11 : MHL, DISPLAY, CAM

TITLE:
FUSION3.0
Drawing Number

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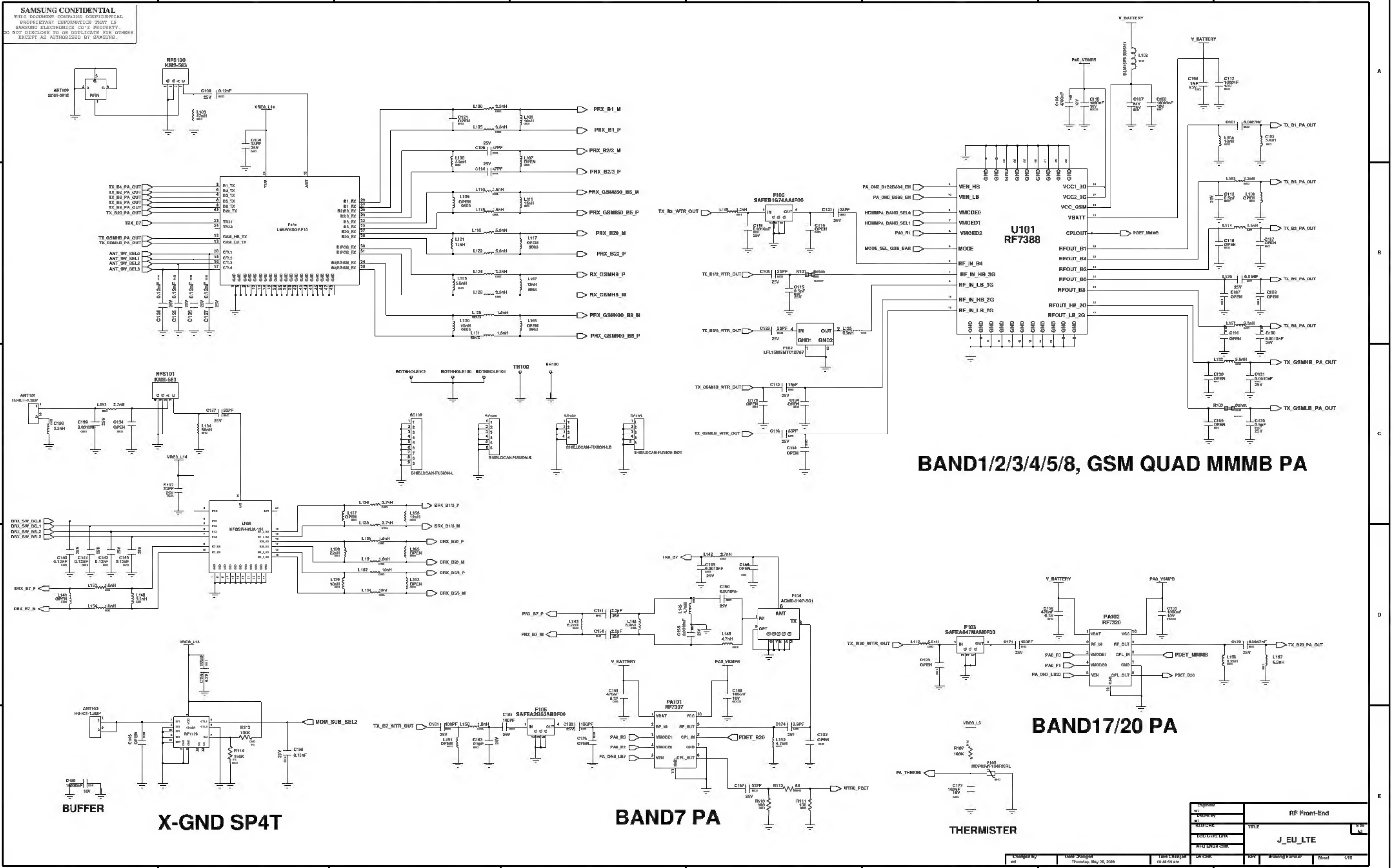
ANT100
RFS100
KMS-583
F101
TX_B1_PA_OUT
TX_B2_PA_OUT
TX_B3_PA_OUT
TX_B5_PA_OUT
TX_B8_PA_OUT
TX_B20_PA_OUT
TRX_B7
TX_GSMHB_PA_OUT
TX_GSMLB_PA_OUT
ANT_SW_SEL0
ANT_SW_SEL1
ANT_SW_SEL2
ANT_SW_SEL3
PRX_B1_M
PRX_B1_P
PRX_B2/3_M
PRX_B2/3_P
PRX_GSM850_B5_M
PRX_GSM850_B5_P
PRX_B20_M
PRX_B20_P
RX_GSMHB_P
RX_GSMHB_M
PRX_GSM900_B8_M
PRX_GSM900_B8_P
ANT101
RFS101
KMS-583
U105
DRX_SW_SEL0
DRX_SW_SEL1
DRX_SW_SEL2
DRX_SW_SEL3
DRX_B1/3_P
DRX_B1/3_M
DRX_B20_P
DRX_B20_M
DRX_B5/8_P
DRX_B5/8_M
DRX_B7_P
DRX_B7_M
BOTHHOLE102
BOTHHOLE100
BOTHHOLE101
TH100
BH100
SC100
SC101
SC102
SC103
ANT102
U103
MDM_SUB_SEL2
BUFFER
X-GND SP4T
V_BATTERY
PA0_VSMPS
U101
RF7388
VEN_HB
VEN_LB
VMODE0
VMODE1
VMODE2
MODE
RF_IN_B4
RF_IN_HB_3G
RF_IN_LB_3G
RF_IN_HB_2G
RF_IN_LB_2G
VCC1_3G
VCC2_3G
VCC_GSM
VBATT
CPLOUT
RFOUT_B1
RFOUT_B4
RFOUT_B2
RFOUT_B5
RFOUT_B8
RFOUT_HB_2G
RFOUT_LB_2G
PDET_MMMB
F100
TX_B3_WTR_OUT
TX_B1/2_WTR_OUT
TX_B5/8_WTR_OUT
TX_GSMHB_WTR_OUT
TX_GSMLB_WTR_OUT
F102
BAND1/2/3/4/5/8, GSM QUAD MMMB PA
TRX_B7
PRX_B7_P
PRX_B7_M
F104
ANT
TX
TX_B7_WTR_OUT
F105
PA101
RF7307
PDET_B20
WTR0_PDET
BAND7 PA
TX_B20_WTR_OUT
F103
PA102
RF7320
TX_B20_PA_OUT
PDET_MMMB
PDET_B20
BAND17/20 PA
VREG_L5
PA_THERM0
THERMISTER
RF Front-End
TITLE
J_EU_LTE
Size A2
Sheet 1/10

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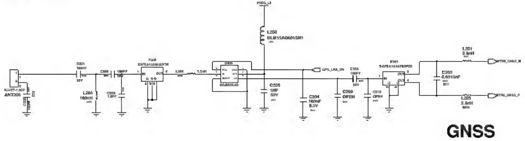
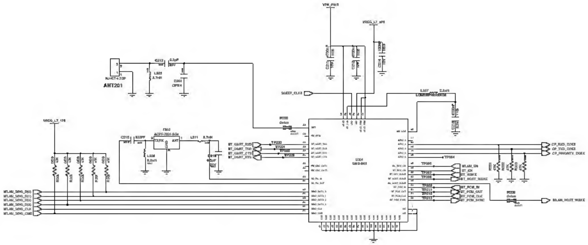
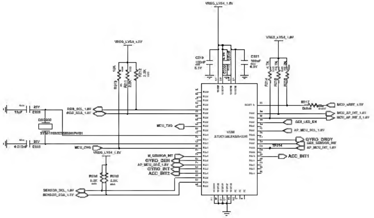
GPS

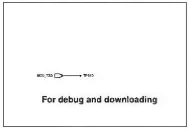

ANT200
L200
BLM15AG601SN1
GPS_LNA_EN
U200
F200
F201
L201
L202
C200
0.001nF
GNSS

BT/WIFI


ANT201
U301
SLEEP_CLK
BT_UART_RXD
BT_UART_TXD
BT_UART_CTS
BT_UART_RTS
CP_RXD_COEX
CP_TXD_COEX
CP_PRIORITY_COEX
WLAN_EN
BT_EN
BT_WAKE
BT_HOST_WAKE
BT_PCM_IN
BT_PCM_OUT
BT_PCM_CLK
BT_PCM_SYNC
WLAN_HOST_WAKE
WLAN_SDIO_D0
WLAN_SDIO_D1
WLAN_SDIO_D2
WLAN_SDIO_D3
WLAN_SDIO_CLK
WLAN_SDIO_CMD

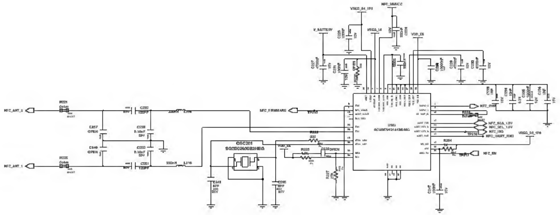
SENSOR HUB


VREG_LVS4_1.8V
MCU_TXD
MCU_CWG
M_SENSOR_INT
GYRO_DEN
AP_MCU_SDA_1.8V
GYRO_INT
ACC_INT2
SENSOR_SCL_1.8V
SENSOR_SDA_1.8V
GPS_LED_EN
GYRO_DRDY
ACC_INT1
TP254

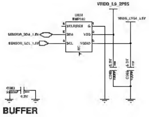

MCU_TXD
TP510
For debug and downloading

(F:128Kb, S:32Kb)

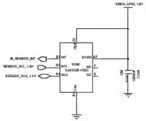
NFC


NFC_ANT_2
NFC_ANT_1
V_BATTERY
VREG_S4_1P8
NFC_FIRMWARE
NFC_SDA_1.8V
NFC_SCL_1.8V
NFC_IRQ
NFC_EN
OSC201
U501

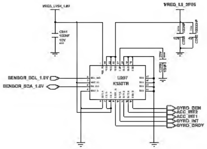
SENSORS


VREG_L9_2P85
VREG_LVS4_1.8V
SENSOR_SDA_1.8V
SENSOR_SCL_1.8V
BMP180
BUFFER

Barometer Sensor


VREG_LVS4_1.8V
M_SENSOR_INT
SENSOR_SCL_1.8V
SENSOR_SDA_1.8V

Magnetic Sensor


VREG_LVS4_1.8V
VREG_L9_2P85
SENSOR_SCL_1.8V
SENSOR_SDA_1.8V
U207
K330TR
GYRO_DEN
ACC_INT2
ACC_INT1
GYRO_INT
GYRO_DRDY

6 Axics Sensor (GYRO,ACCEL)

TITLE: J_EU_LTE

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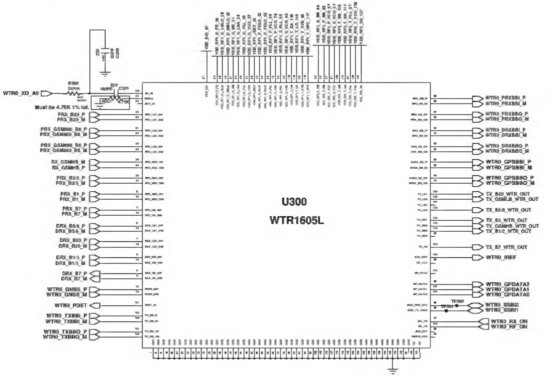
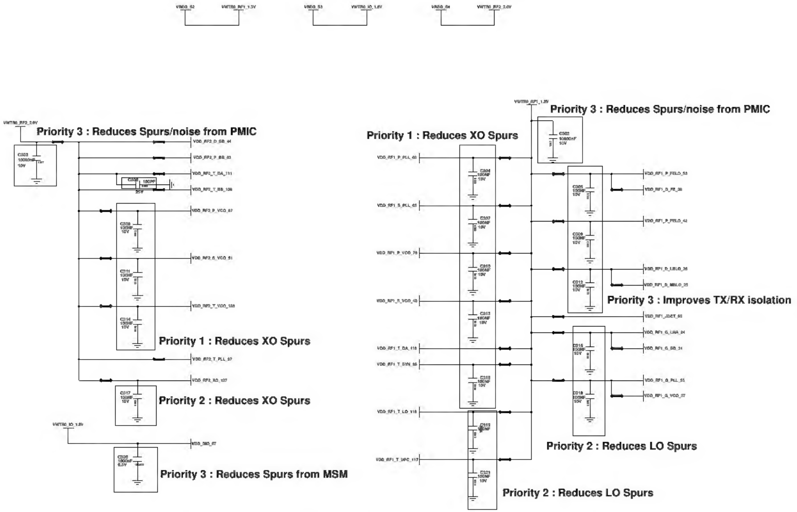
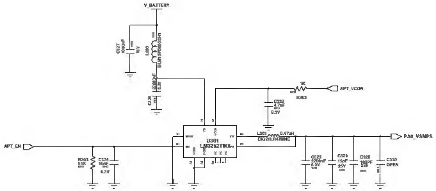
TRANCEIVER


U300
WTR1605L
C300
33PF
25V
R300
0ohm
C301
100PF
25V
R301
4.75K
Must be 4.75K 1% tol.
WTR0_XO_A0
PRX_B20_P
PRX_B20_M
PRX_GSM900_B8_P
PRX_GSM900_B8_M
PRX_GSM850_B5_P
PRX_GSM850_B5_M
RX_GSMHB_M
RX_GSMHB_P
PRX_B2/3_P
PRX_B2/3_M
PRX_B1_P
PRX_B1_M
PRX_B7_P
PRX_B7_M
DRX_B5/8_P
DRX_B5/8_M
DRX_B20_P
DRX_B20_M
DRX_B1/3_P
DRX_B1/3_M
DRX_B7_P
DRX_B7_M
WTR0_GNSS_P
WTR0_GNSS_M
WTR0_PDET
WTR0_TXBBI_P
WTR0_TXBBI_M
WTR0_TXBBQ_P
WTR0_TXBBQ_M
WTR0_PRXBBI_P
WTR0_PRXBBI_M
WTR0_PRXBBQ_P
WTR0_PRXBBQ_M
WTR0_DRXBBI_P
WTR0_DRXBBI_M
WTR0_DRXBBQ_P
WTR0_DRXBBQ_M
WTR0_GPSBBI_P
WTR0_GPSBBI_M
WTR0_GPSBBQ_P
WTR0_GPSBBQ_M
TX_B20_WTR_OUT
TX_GSMLB_WTR_OUT
TX_B5/8_WTR_OUT
TX_B3_WTR_OUT
TX_GSMHB_WTR_OUT
TX_B1/2_WTR_OUT
TX_B7_WTR_OUT
WTR0_IREF
WTR0_GPDATA2
WTR0_GPDATA1
WTR0_GPDATA0
TP300
TP301
WTR0_SSBI2
WTR0_SSBI1
WTR0_RX_ON
WTR0_RF_ON


VREG_S2
VWTR0_RF1_1.3V
VREG_S3
VWTR0_IO_1.8V
VREG_S4
VWTR0_RF2_2.0V
VWTR0_RF2_2.0V
Priority 3 : Reduces Spurs/noise from PMIC
C303
10000nF
10V
VDD_RF2_D_SB_44
VDD_RF2_P_BB_83
VDD_RF2_T_DA_111
C306
100PF
25V
VDD_RF2_T_BB_108
VDD_RF2_P_VCO_67
C308
100NF
10V
VDD_RF2_S_VCO_51
C311
100NF
10V
VDD_RF2_T_VCO_126
C314
100NF
10V
Priority 1 : Reduces XO Spurs
VDD_RF2_T_PLL_97
VDD_RF2_XO_127
C317
100NF
10V
Priority 2 : Reduces XO Spurs
VWTR0_IO_1.8V
VDD_DIG_87
C320
1000nF
6.3V
Priority 3 : Reduces Spurs from MSM
VWTR0_RF1_1.3V
Priority 3 : Reduces Spurs/noise from PMIC
C302
10000nF
10V
Priority 1 : Reduces XO Spurs
VDD_RF1_P_PLL_60
C304
100NF
10V
VDD_RF1_S_PLL_65
C307
100NF
10V
VDD_RF1_P_VCO_76
C310
100NF
10V
VDD_RF1_S_VCO_40
C313
100NF
10V
VDD_RF1_T_DA_116
VDD_RF1_T_SYN_98
C318
100NF
10V
VDD_RF1_T_LO_118
C319
100NF
VDD_RF1_T_UPC_117
C321
100NF
10V
Priority 2 : Reduces LO Spurs
VDD_RF1_P_FELO_53
VDD_RF1_D_FE_38
C305
100NF
10V
VDD_RF1_P_FELO_42
C309
100NF
10V
VDD_RF1_D_LBLO_26
VDD_RF1_D_MBLO_25
C312
100NF
10V
Priority 3 : Improves TX/RX isolation
VDD_RF1_JDET_66
VDD_RF1_G_LNA_24
VDD_RF1_G_BB_31
C315
100NF
10V
VDD_RF1_G_PLL_55
VDD_RF1_G_VCO_37
C316
100NF
10V
Priority 2 : Reduces LO Spurs

All decaps should have star connnections


V_BATTERY
C327
1000nF
16V
L300
BLM15PD600SN1
C328
22000nF
6.3V
1K
R302
APT_VCON
C323
4.7nF
6.3V
U301
LM3262TMX
L301
0.47uH
CIG21LR47MNE
PA0_VSMPS
APT_EN
R303
51K
C324
10nF
6.3V
C325
2200nF
6.3V
C329
15pF
25V
C328
100PF
25V
C330
OPEN

PA DCDC

Engineer
wd
Drawn By
wd
R&D CHK
DOC CTRL CHK
MFG ENGR CHK
QA CHK

Tranceiver

TITLE
J_EU_LTE
SIZE
A2

Changed By
wd
Date Changed
Thursday, May 28, 2009
Time Changed
10:48:53 am
REV
Drawing Number
Sheet 1/10

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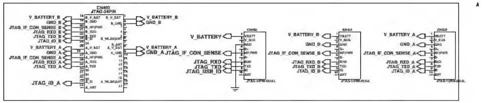
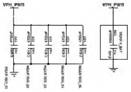
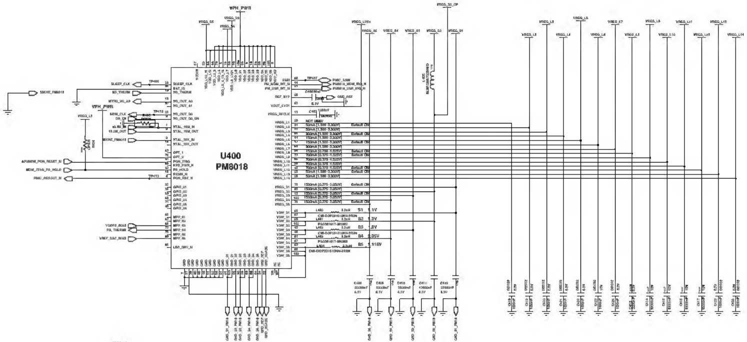
CP PMIC

PCB JTAG


CN403
JTAG-34PIN
V_BATTERY_B
GND_B
JTAG_IF_CON_SENSE_B
JTAG_RXD_B
JTAG_TXD_B
JTAG_ID_B
V_BATTERY_A
GND_A
JTAG_IF_CON_SENSE_A
JTAG_RXD_A
JTAG_TXD_A
JTAG_ID_A
V_BATTERY_B
GND_B
V_BATTERY_A
GND_A
JTAG_IF_CON_SENSE
V_BATTERY
JTAG_RXD
JTAG_TXD
JTAG_USB_ID
CN401
CN402
VBATT
V_BUS
GND
D+
MP PWR
D-
RXD
TXD
ID
ANT
A


VPH_PWR
VPH_PWR


VPH_PWR
VREG_S5_CP
VREG_S5
VREG_S4
U400
PM8018
SLEEP_CLK
BAT_ID
XO_THERM
XO_OUT_A0
XO_OUT_A1
XO_OUT_D0
XO_OUT_D0_EN
XTAL_19M_IN
XTAL_19M_OUT
XTAL_32K_IN
XTAL_32K_OUT
OPT_1
OPT_2
PON_TRIG
KPD_PWR_N
PS_HOLD
RESIN_N
PON_RST_N
GPIO_01
GPIO_02
GPIO_03
GPIO_04
GPIO_05
GPIO_06
MPP_01
MPP_02
MPP_03
MPP_04
MPP_05
MPP_06
LED_DRV_N
SLEEP_CLK
XO_THERM
MDM_CLK
19.2M_OUT
APQMDM_PON_RESET_N
MDM_JTAG_PS_HOLD
PMIC_RESOUT_N
VDDPX_BIAS
PA_THERM
VREF_DAC_BIAS
SSBI
PM_MDM_INT_N
PM_USR_INT_N
REF_BYP
VOUT_EVSI
VREG_RFCLK
VREG_L1
VREG_L2
VREG_L3
VREG_L4
VREG_L5
VREG_L6
VREG_L7
VREG_L8
VREG_L9
VREG_L10
VREG_L11
VREG_L12
VREG_L13
VREG_L14
VREG_S1
VREG_S2
VREG_S3
VREG_S4
VREG_S5
VSW_S1
VSW_S2
VSW_S3
VSW_S4
VSW_S5
TP407
PMIC_SSBI
PM8018_MDM_IRQ_N
PM8018_USR_IRQ_N
NOT USED
50mA [1.500~3.300V]
300mA [1.500~3.300V]
150mA [1.500~3.300V]
150mA [0.750~1.525V]
700mA [0.375~1.525V]
1500mA [0.375~3.050V]
Default ON
L400
L401
L402
L403
L404
S1 1.1V
B2 1.3V
B3 1.8V
B4 2.05V
B5 1.115V
VREG_LVS1
VREG_S6
VREG_S4
VREG_S3
VREG_S2
VREG_S1
VREG_L2
VREG_L3
VREG_L4
VREG_L5
VREG_L6
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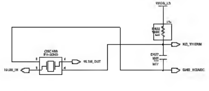
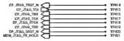
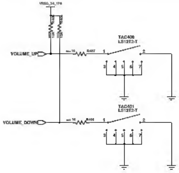
VREG_L9
VREG_L10
VREG_L11
VREG_L12
VREG_L13
VREG_L14


VREG_L5
OSC400
19.2M_IN
19.2M_OUT
XO_THERM
GND_XOADC


CP_JTAG_TRST_N
CP_JTAG_TDI
CP_JTAG_TMS
CP_JTAG_TCK
CP_JTAG_RTCK
CP_JTAG_TDO
CP_JTAG_SRST_N
MDM_JTAG_PS_HOLD
TP414
TP415
TP416
TP417
TP418
TP419
TP420
TP421

CP JTAG / Volume key Connector


VREG_S4_1P8
VOLUME_UP
VOLUME_DOWN
R407
R408
TAC400
LS12T2-T
TAC401
LS12T2-T
1
2
3
4
5
6
7

TITLE:
J_EU_LTE

Drawing Number

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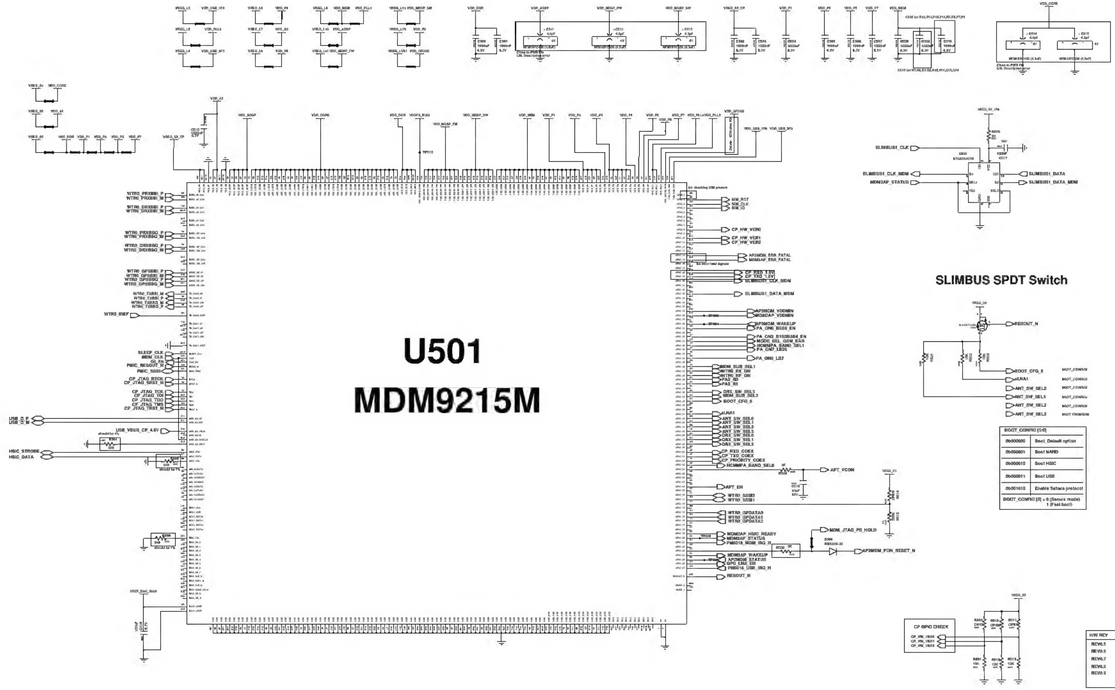
CP PART


U501
MDM9215M
SLIMBUS SPDT Switch
VDD_CORE
VDD_A2
VDD_ADSP
VDD_DDR
VDDPX_BUSS
VDD_MDSP_FW
VDD_MDSP_SW
VDD_MEM
VDD_P1
VDD_P3
VDD_P5
VDD_P4
VDD_P6
VDD_P7
VDD_PLL2
VDD_QFUSE
VDD_USB_1P8
VDD_USB_3P3
VREG_S3_CP
VREG_S4
VREG_S5
VREG_S6
TP510
for checking UIM presence
WTR0_PRXBBI_P
WTR0_PRXBBI_M
WTR0_DRXBBI_P
WTR0_DRXBBI_M
WTR0_PRXBBQ_P
WTR0_PRXBBQ_M
WTR0_DRXBBQ_P
WTR0_DRXBBQ_M
WTR0_GPSBBI_P
WTR0_GPSBBI_M
WTR0_GPSBBQ_P
WTR0_GPSBBQ_M
WTR0_TXBBI_M
WTR0_TXBBI_P
WTR0_TXBBQ_M
WTR0_TXBBQ_P
WTR0_IREF
SLEEP_CLK
MDM_CLK
DO_EN
PMIC_RESOUT_N
PMIC_SSBI
CP_JTAG_RTCK
CP_JTAG_SRST_N
CP_JTAG_TCK
CP_JTAG_TDI
CP_JTAG_TDO
CP_JTAG_TMS
CP_JTAG_TRST_N
USB_D_P
USB_D_M
USB_VBUS_CP_4.8V
should be 1%
R504
HSIC_STROBE
HSIC_DATA
R505
should be 1%
R506
should be 1%
VREF_DAC_BIAS
SIM_RST
SIM_CLK
SIM_IO
CP_HW_VER0
CP_HW_VER1
CP_HW_VER2
AP2MDM_ERR_FATAL
MDM2AP_ERR_FATAL
for error fatal signals
CP_RXD_1.8V
CP_TXD_1.8V
SLIMBUS1_CLK_MDM
SLIMBUS1_DATA_MDM
AP2MDM_VDDMIN
MDM2AP_VDDMIN
TP503
TP504
AP2MDM_WAKEUP
PA_ON0_B5B8_EN
PA_ON2_B1B2B3B4_EN
MODE_SEL_GSM_BAR
HCMMPA_BAND_SEL1
PA_ON7_LB20
PA_ON8_LB7
MDM_SUB_SEL1
WTR0_RX_ON
WTR0_RF_ON
PA0_R0
PA0_R1
DRX_SW_SEL3
MDM_SUB_SEL2
BOOT_CFG_5
eLNA1
ANT_SW_SEL0
ANT_SW_SEL1
ANT_SW_SEL2
ANT_SW_SEL3
DRX_SW_SEL0
DRX_SW_SEL1
DRX_SW_SEL2
CP_RXD_COEX
CP_TXD_COEX
CP_PRIORITY_COEX
HCMMPA_BAND_SEL0
APT_VCON
APT_EN
WTR0_SSBI2
WTR0_SSBI1
VREG_S3
R514
R515
WTR0_GPDATA0
WTR0_GPDATA1
WTR0_GPDATA2
MDM_JTAG_PS_HOLD
MDM2AP_HSIC_READY
MDM2AP_STATUS
PM8018_MDM_IRQ_N
TP508
D500
AP2MDM_PON_RESET_N
MDM2AP_WAKEUP
AP2MDM_STATUS
GPS_LNA_EN
PM8018_USB_IRQ_N
RESOUT_N
SLIMBUS1_CLK
SLIMBUS1_CLK_MDM
MDM2AP_STATUS
SLIMBUS1_DATA
SLIMBUS1_DATA_MDM
U500
VREG_S4_1P8
RESOUT_N
R501
BOOT_CFG_5
eLNA1
ANT_SW_SEL0
ANT_SW_SEL1
ANT_SW_SEL2
ANT_SW_SEL3
BOOT_CONFIG5
BOOT_CONFIG3
BOOT_CONFIG2
BOOT_CONFIG1
BOOT_CONFIG0
BOOT FROM ROM
CP GPIO CHECK
CP_HW_VER0
CP_HW_VER1
CP_HW_VER2
VREG_L6

BOOT_CONFIG [5:0]	
0b000000	Boot_Default option
0b000001	Boot NAND
0b000010	Boot HSIC
0b000011	Boot USB
0b001010	Enable Sahara protocol

BOOT_CONFIG [6] = 0 (Secure mode)
1 (Fast boot)

H/W REV	VER2	VER1	VER0
REV0.5	0	0	0
REV0.6	0	0	0
REV0.7	0	0	0
REV0.8	0	0	0
REV0.9	0	0	0

REV0.6 CP VDD_P5 changed VREG_S3 (1.8V) -> VREG_L5 (Due. SIM Dual Voltage Support)

TITLE:
J_EU_LTE

Drawing Number

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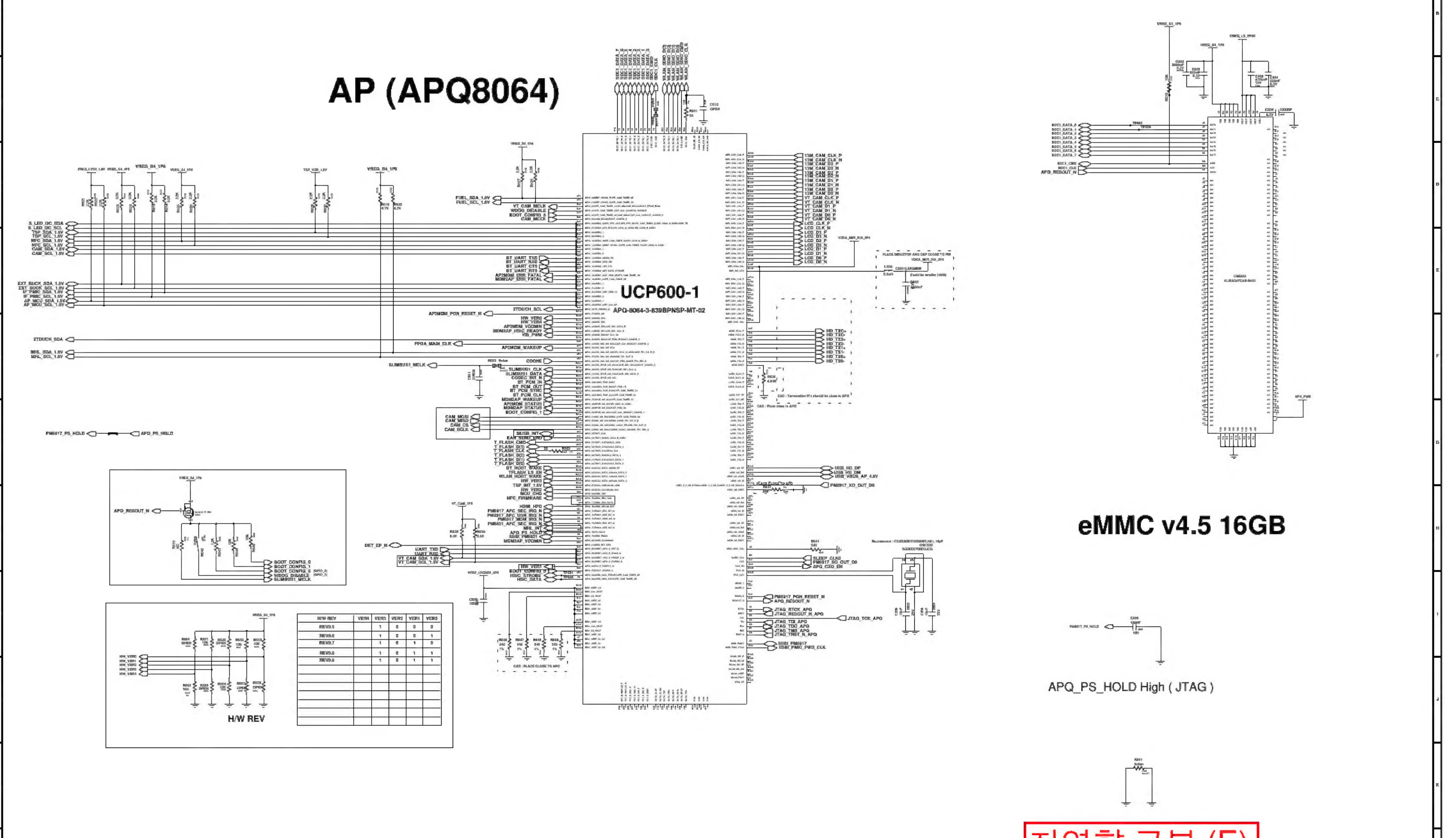
AP (APQ8064)
UCP600-1
APQ-8064-3-839BPNSP-MT-02
eMMC v4.5 16GB
APQ_PS_HOLD High (JTAG)
H/W REV
S_LED_I2C_SDA
S_LED_I2C_SCL
TSP_SDA_1.8V
TSP_SCL_1.8V
NFC_SDA_1.8V
NFC_SCL_1.8V
CAM_SDA_1.8V
CAM_SCL_1.8V
EXT_BUCK_SDA_1.8V
EXT_BUCK_SCL_1.8V
IF_PMIC_SDA_1.8V
IF_PMIC_SCL_1.8V
AP_MCU_SDA_1.8V
AP_MCU_SCL_1.8V
2TOUCH_SDA
MHL_SDA_1.8V
MHL_SCL_1.8V
PM8917_PS_HOLD
APQ_PS_HOLD
APQ_RESOUT_N
BOOT_CONFIG_0
BOOT_CONFIG_1
BOOT_CONFIG_6
WDOG_DISABLE
SLIMBUS1_MCLK
FUEL_SDA_1.8V
FUEL_SCL_1.8V
VT_CAM_MCLK
CAM_MCLK
BT_UART_TXD
BT_UART_RXD
BT_UART_CTS
BT_UART_RTS
AP2MDM_ERR_FATAL
MDM2AP_ERR_FATAL
2TOUCH_SCL
AP2MDM_PON_RESET_N
FPGA_MAIN_CLK
AP2MDM_WAKEUP
SLIMBUS1_MCLK
CODEC
CAM_MOSI
CAM_MISO
CAM_CS
CAM_SCLK
DET_EP_N
UART_TXD
UART_RXD
VT_CAM_SDA_1.8V
VT_CAM_SCL_1.8V
HSIC_STROBE
HSIC_DATA
13M_CAM_CLK_P
13M_CAM_CLK_N
VT_CAM_CLK_P
VT_CAM_CLK_N
LCD_CLK_P
LCD_CLK_N
HD_TXC+
HD_TXC-
HD_TX2+
HD_TX2-
HD_TX1+
HD_TX1-
HD_TX0+
HD_TX0-
USB_HS_DP
USB_HS_DM
USB_VBUS_AP_4.8V
PM8917_XO_OUT_D0
SLEEP_CLK0
APQ_CXO_EN
PM8917_PON_RESET_N
JTAG_RTCK_APQ
JTAG_RESOUT_N_APQ
JTAG_TCK_APQ
JTAG_TDI_APQ
JTAG_TDO_APQ
JTAG_TMS_APQ
JTAG_TRST_N_APQ
SSBI_PM8917
SSBI_PMIC_FWD_CLK
SDC1_DATA_0
SDC1_DATA_1
SDC1_DATA_2
SDC1_DATA_3
SDC1_DATA_4
SDC1_DATA_5
SDC1_DATA_6
SDC1_DATA_7
SDC1_CMD
SDC1_CLK
APQ_RESOUT_N
HW_VER0
HW_VER1
HW_VER2
HW_VER3
HW_VER4

H/W REV	VER4	VER3	VER2	VER1	VER0
REV0.5		1	0	0	0
REV0.6		1	0	0	1
REV0.7		1	0	1	0
REV0.8		1	0	1	1
REV0.9		1	0	1	1

지역향 구분 (E)

TITLE: J_EU_LTE

Drawing Number 3/8

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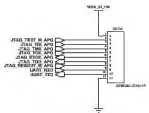
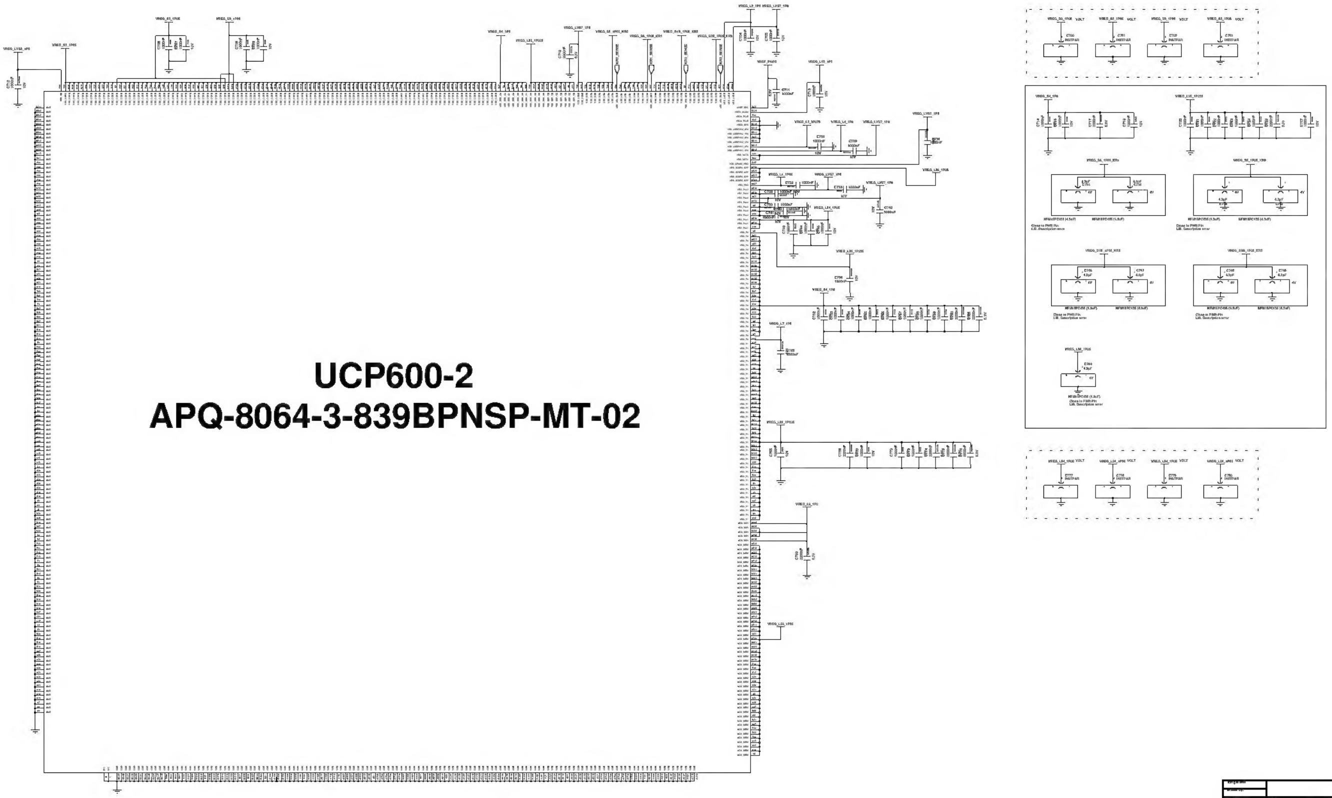
APQ8064 - POWER


VREG_S4_1P8
JTAG_TRST_N_APQ
JTAG_TDI_APQ
JTAG_TMS_APQ
JTAG_TCK_APQ
JTAG_RTCK_APQ
JTAG_TDO_APQ
JTAG_REBOUT_N_APQ
UART_RXD
UART_TXD

AP JTAG Connector


UCP600-2
APQ-8064-3-839BPNSP-MT-02
VOLT

TITLE: J_EU_LTE

Drawing Number

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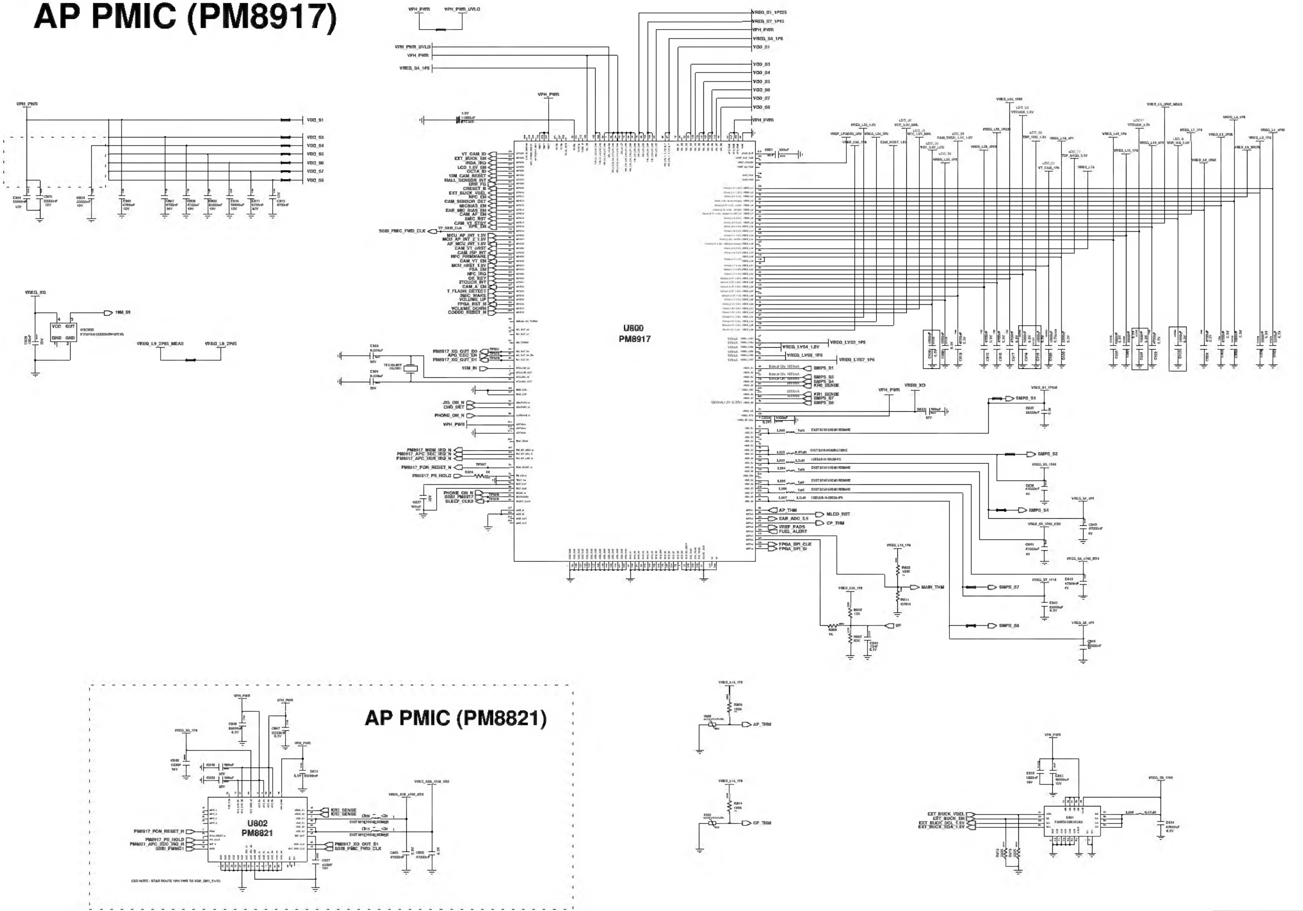
AP PMIC (PM8917)

AP PMIC (PM8821)


U800
PM8917
U802
PM8821
VPH_PWR
VPH_PWR_UVLO
VREG_S1_1P225
VREG_S7_1P15
VREG_S4_1P8
VDD_S1
VDD_S3
VDD_S4
VDD_S5
VDD_S6
VDD_S7
VDD_S8
VREG_XO
19M_IN
VREG_L9_2P85_MEAS
VREG_L9_2P85
VT_CAM_ID
EXT_BUCK_EN
IRDA_IRQ
LCD_1.8V_EN
OCTA_ID
15M_CAM_RESET
HALL_SENSOR_INT
ERR_FG
CRESET_B
EXT_BUCK_VSEL
NFC_EN
CAM_SENSOR_DET
MICBIAS_EN
EAR_MIC_BIAS_EN
CAM_AF_EN
2MIC_RST
CAM_VT_STBY
SPK_EN
SSBI_PMIC_FWD_CLK
MCU_AP_INT_1.8V
MCU_AP_INT_2_1.8V
AP_MCU_INT_1.8V
CAM_VT_nRST
CAM_ISP_INT
NFC_FIRMWARE
CAM_VT_EN
MCU_nRST_1.8V
PSA_EN
NFC_IRQ
OK_KEY
2TOUCH_INT
CAM_A_EN
T_FLASH_DETECT
2MIC_WAKE
VOLUME_UP
FPGA_RST_M
VOLUME_DOWN
CODEC_RESET_N
PM8917_XO_OUT_D0
APQ_CXO_EN
PM8917_XO_OUT_D1
JIG_ON_N
CHG_DET
PHONE_ON_N
PM8917_MDM_IRQ_N
PM8917_APC_SEC_IRQ_N
PM8917_APC_USR_IRQ_N
PM8917_PON_RESET_N
PM8917_PS_HOLD
SSBI_PM8917
SLEEP_CLK0
VREG_LVS4_1.8V
VREG_LVS3_1P8
VREG_LVS8_1P8
VREG_LVS7_1P8
SMPS_S1
SMPS_S3
SMPS_S4
SMPS_S7
SMPS_S8
KR0_SENSE
KR1_SENSE
AP_THM
EAR_ADC_3.5
MLCD_RST
CP_THM
VREF_PADS
FUEL_ALERT
FPGA_BPI_CLK
FPGA_BPI_SI
MAIN_THM
VF
KR2_SENSE
KR3_SENSE
PM8917_PON_RESET_N
PM8917_PS_HOLD
PM8821_APC_SEC_IRQ_N
SSBI_PM8821
PM8917_XO_OUT_D1
SSBI_PMIC_FWD_CLK
CAD NOTE : STAR ROUTE VPH PWR TO VDD_QDY_S1/S2
EXT_BUCK_VSEL
EXT_BUCK_EN
EXT_BUCK_SCL_1.8V
EXT_BUCK_SDA_1.8V
U801
VREG_S5_1P05

TITLE: J_EU_LTE

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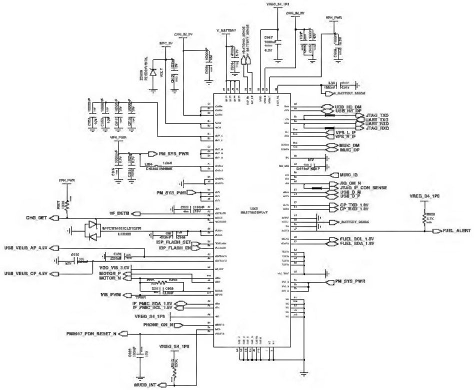
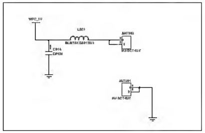
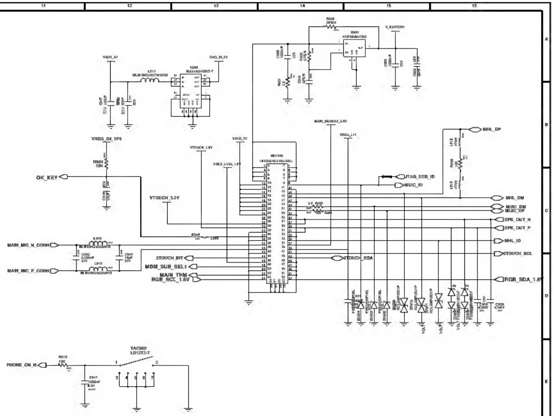
IF PMIC PART


VREG_S4_1P8
CHG_IN_5V
VPH_PWR
V_BATTERY
MPP_5V
PM_SYS_PWR
CHG_DET
VF_DETB
USB_VBUS_AP_4.9V
USB_VBUS_CP_4.9V
VDD_VIB_3.6V
MOTOR_P
MOTOR_N
VIB_PWM
IF_PMIC_SDA_1.8V
IF_PMIC_SCL_1.8V
PHONE_ON_N
PM8917_PON_RESET_N
SRUSB_INT
ISP_FLASH_SET
ISP_FLASH_EN
USB_HS_DM
USB_HS_DP
JTAG_TXD
UART_TXD
UART_RXD
JTAG_RXD
VPS_L_IF
VPS_R_IF
MUIC_DM
MUIC_DP
MUIC_ID
JIG_ON_N
JTAG_IF_CON_SENSE
USB_D_M
USB_D_P
CP_TXD_1.8V
CP_RXD_1.8V
FUEL_SCL_1.8V
FUEL_SDA_1.8V
FUEL_ALERT

WIRELESS CHARGING



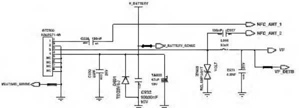

V_BATTERY
VBUS_5V
CHG_IN_5V
VREG_S4_1P8
OK_KEY
MAIN_MIC_N_CONN
MAIN_MIC_P_CONN
VTOUCH_3.3V
VTOUCH_1.8V
2TOUCH_INT
MDM_SUB_SEL1
MAIN_THM
RGB_SCL_1.8V
2TOUCH_SDA
2TOUCH_SCL
RGB_SDA_1.8V
MHL_DP
MHL_DM
MUIC_DM
MUIC_DP
SPK_OUT_N
SPK_OUT_P
MHL_ID
JTAG_USB_ID
MUIC_ID
PHONE_ON_N
TAC900

SUB FPCB IF CONN.

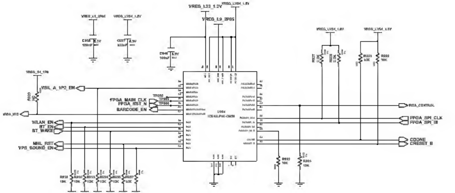
(11PIN USB / Main ANT / SPK)

Battery

Connector


V_BATTERY
NFC_ANT_1
NFC_ANT_2
VF
VF_DETB

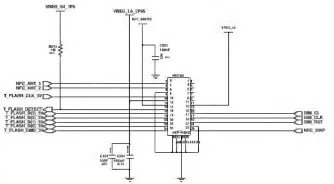
IRLED


VREG_L23_1.2V
VREG_L9_2P85
FPGA_MAIN_CLK
FPGA_RST_N
BARCODE_EN
WLAN_EN
BT_EN
BT_WAKE
MHL_RST
VPS_SOUND_EN
FPGA_SPI_CLK
FPGA_SPI_SI
CDONE
CRESET_B

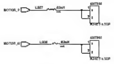
SIM / SD CARD

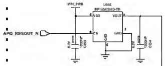
VREG_L6 & VREG_L7 provide both 1.8V & 2.95V depends on the SD card support mode

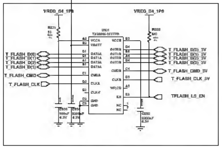
Located on FPCB : Decaps,ESD Protector, Termination-R


VREG_S4_1P8
VREG_L6_2P95
NFC_ANT_1
NFC_ANT_2
T_FLASH_CLK_3V
T_FLASH_DETECT
T_FLASH_D(2)_3V
T_FLASH_D(3)_3V
T_FLASH_D(1)_3V
T_FLASH_D(0)_3V
T_FLASH_CMD_3V
SIM_IO
SIM_CLK
SIM_RST
NFC_SWP

MOTOR




APQ_RESOUT_N


VREG_S4_1P8
T_FLASH_D(0)
T_FLASH_D(1)
T_FLASH_D(2)
T_FLASH_D(3)
T_FLASH_CMD
T_FLASH_CLK
T_FLASH_D(0)_3V
T_FLASH_D(1)_3V
T_FLASH_D(2)_3V
T_FLASH_D(3)_3V
T_FLASH_CMD_3V
T_FLASH_CLK_3V
TFLASH_LS_EN

LEVEL SHIFTER

Engineer:
Jack
Drawn by:
Jack
R&D CHK:
DOC CTRL CHK:
MFG ENGR CHK:
QA CHK:
TITLE:
J_EU_LTE
SIZE:
A1
Changed by:
Jack
Date Changed:
Monday, November 29, 2005
Time Changed:
[illegible]
REV:
Drawing Number:

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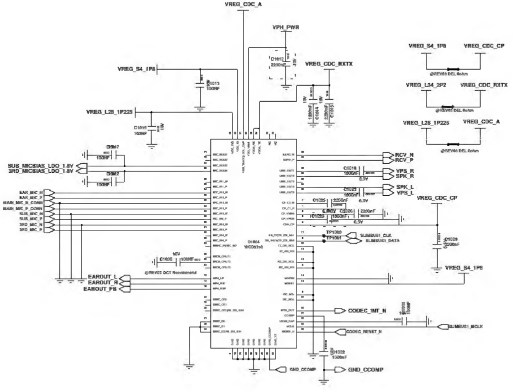
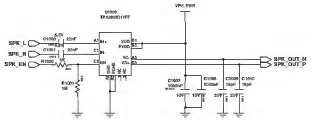
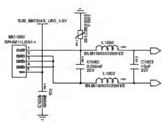
Audio PART


VREG_CDC_A
VPH_PWR
VREG_CDC_RXTX
VREG_S4_1P8
VREG_L25_1P225
SUB_MICBIAS_LDO_1.8V
3RD_MICBIAS_LDO_1.8V
EAR_MIC_N
EAR_MIC_P
MAIN_MIC_N_CONN
MAIN_MIC_P_CONN
SUB_MIC_N
SUB_MIC_P
3RD_MIC_N
3RD_MIC_P
EAROUT_L
EAROUT_R
EAROUT_FB
U1004
WCD9310
RCV_N
RCV_P
VPS_R
SPK_R
SPK_L
VPS_L
VREG_CDC_CP
TP1000
TP1001
SLIMBUS1_CLK
SLIMBUS1_DATA
CODEC_INT_N
SLIMBUS1_MCLK
CODEC_RESET_N
GND_CCOMP
VREG_S4_1P8
VREG_CDC_CP
VREG_L34_2P2
VREG_CDC_RXTX
VREG_L25_1P225
VREG_CDC_A
@REV03 DEL 0ohm
@REV03 DCT Recommend

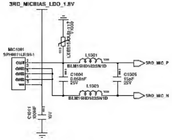
AUDIO CODEC


U1000
TPA2028D1YFF
VPH_PWR
SPK_L
SPK_R
SPK_EN
SPK_OUT_N
SPK_OUT_P
R1001
1M

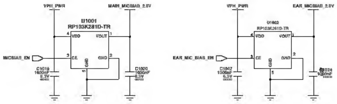
SPK AUDIO AMP


SUB_MICBIAS_LDO_1.8V
MIC1000
SPH0611LR5H-1
L1000
L1002
BLM15HG102SN1D
SUB_MIC_P
SUB_MIC_N

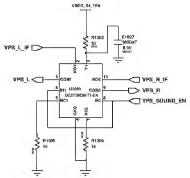
2nd MIC


3RD_MICBIAS_LDO_1.8V
MIC1001
SPH0611LR5H-1
L1001
L1003
BLM15HG102SN1D
3RD_MIC_P
3RD_MIC_N

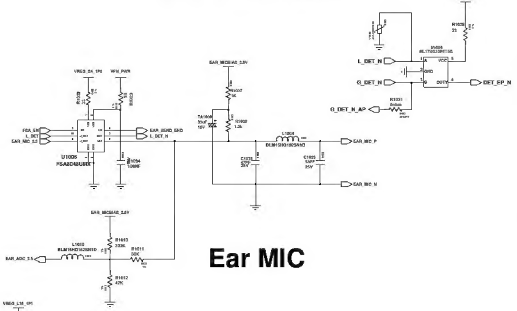
3rd MIC


VPH_PWR
MAIN_MICBIAS_2.8V
U1001
RP103K281D-TR
MICBIAS_EN
VPH_PWR
EAR_MICBIAS_2.8V
U1002
RP103K281D-TR
EAR_MIC_BIAS_EN

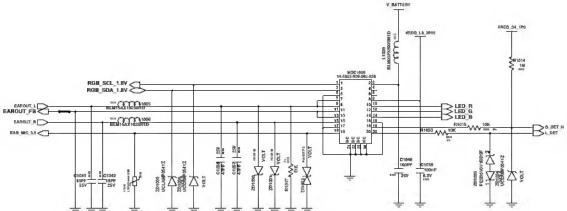
MIC Bias LDOs


VREG_S4_1P8
R1002
C1027
VPS_L_IF
VPS_L
U1003
VPS_R_IF
VPS_R
VPS_SOUND_EN
R1005
R1006

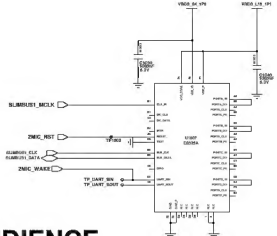
Audio Switch


VREG_S4_1P8
R1028
U1009
L_DET_N
G_DET_N
G_DET_N_AP
DET_EP_N
R1031
EAR_MICBIAS_2.8V
VREG_S4_1P8
VPH_PWR
FSA_EN
L_DET
EAR_MIC_3.5
U1005
FSA8048UMX
EAR_SEND_END
L_DET_N
L1004
BLM15HG102SN1D
EAR_MIC_P
EAR_MIC_N
EAR_MICBIAS_2.8V
L1010
BLM15HD102SN1D
EAR_ADC_3.5
R1010
R1011
R1012
47K

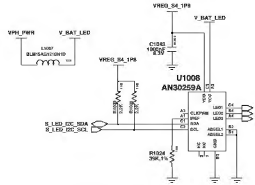
Ear MIC


V_BATTERY
VREG_L8_2P85
VREG_S4_1P8
RGB_SCL_1.8V
RGB_SDA_1.8V
EAROUT_L
EAROUT_FB
EAROUT_R
EAR_MIC_3.5
L1005
L1006
BLM15AX102SN1D
HDC1000
LED_R
LED_G
LED_B
R1014
1M
R1030
R1015
10K
G_DET_N
L_DET

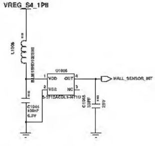
RCV + E/J + SVC LED


VREG_S4_1P8
VREG_L18_1P1
SLIMBUS1_MCLK
2MIC_RST
TP1002
SLIMBUS1_CLK
SLIMBUS1_DATA
2MIC_WAKE
TP_UART_SIN
TP_UART_SOUT
U1007
ES325A

AUDIENCE


VREG_S4_1P8
V_BAT_LED
VPH_PWR
V_BAT_LED
L1007
BLM15AG121SN1D
VREG_S4_1P8
U1008
AN30259A
S_LED_I2C_SDA
S_LED_I2C_SCL
LED_R
LED_G
LED_B
R1024
39K,1%

SVC LED DRIVER


VREG_S4_1P8
L1008
U1006
HALL_SENSOR_INT
C1044
100nF
6.3V

HALL SENSOR

TITLE:
J_EU_LTE

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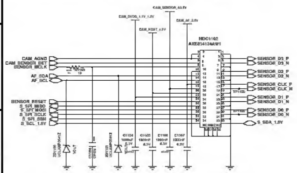
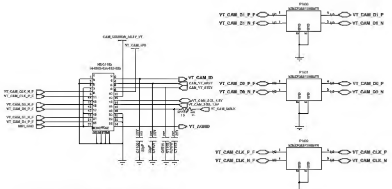
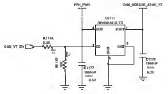
CAMERA PART


HDC1102
SENSOR_D3_P
SENSOR_D3_N
SENSOR_D2_P
SENSOR_D2_N
SENSOR_CLK_P
SENSOR_CLK_N
SENSOR_D1_P
SENSOR_D1_N
SENSOR_D0_P
SENSOR_D0_N
S_SDA_1.8V
CAM_AGND
AF_SDA
AF_SCL
SENSOR_RESET
S_SCL_1.8V

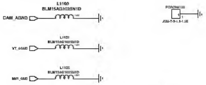
13M_Camera Connector (MIPI)

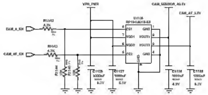

VT_CAM_D1_P_F
VT_CAM_D1_N_F
VT_CAM_D1_P
VT_CAM_D1_N
VT_CAM_D0_P_F
VT_CAM_D0_N_F
VT_CAM_D0_P
VT_CAM_D0_N
VT_CAM_CLK_P_F
VT_CAM_CLK_N_F
VT_CAM_CLK_P
VT_CAM_CLK_N
VT_CAM_ID
VT_AGND

VT_CAMERA (2M)

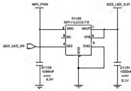

VPH_PWR
CAM_VT_EN

VT_CAMERA LDO

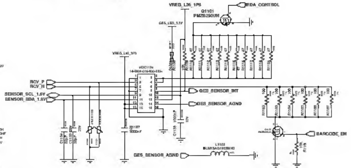

L1100
BLM15AG102SN1D
CAM_AGND
VT_AGND


VPH_PWR
CAM_S_EN
CAM_AF_EN

CAM DCDC/LDO

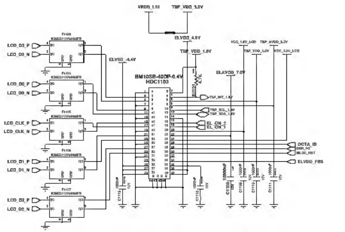

VPH_PWR
GES_LED_EN

Gesture Sensor LDO

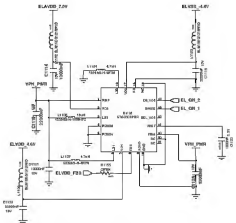

VREG_L36_1P8
Q1101
IRDA_CONTROL
RCV_P
RCV_N
SENSOR_SCL_1.8V
SENSOR_SDA_1.8V
GES_SENSOR_INT
GES_SENSOR_AGND
BARCODE_EN
L1102
BLM15AG102SN1D

GESTURE + IRLED

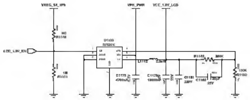
DISPLAY PART


VREG_L18
TSP_VDD_3.3V
ELVDD_4.6V
ELVSS_-4.4V
ELAVDD_7.0V
HDC1103
LCD_D3_P
LCD_D3_N
LCD_D0_P
LCD_D0_N
LCD_CLK_P
LCD_CLK_N
LCD_D1_P
LCD_D1_N
LCD_D2_P
LCD_D2_N
TSP_INT_1.8V
TSP_SCL_1.8V
TSP_SDA_1.8V
EL_ON_2
EL_ON_1
OCTA_ID
ELVDD_FBS

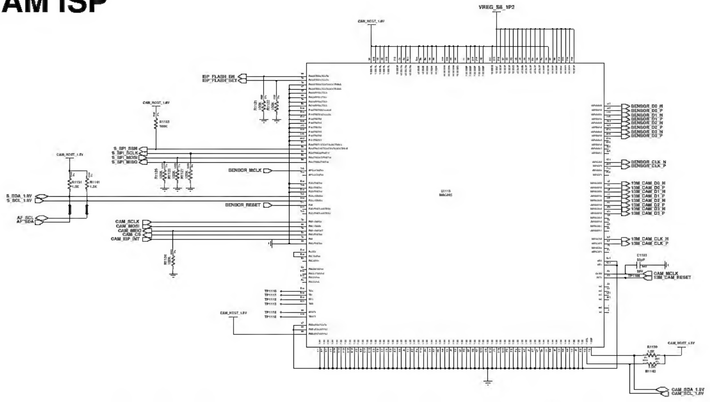
OCTA Conector(MIPI)


ELAVDD_7.0V
ELVSS_-4.4V
VPH_PWR
ELVDD_4.6V
EL_ON_2
EL_ON_1
ELVDD_FBS
VPH_PWR

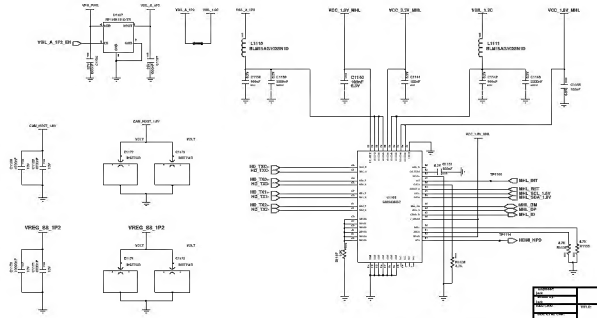
FHD OCTA DC-DC


VREG_S4_1P8
VPH_PWR
VCC_1.8V_LCD
LDO_LSW_EN

CAM ISP

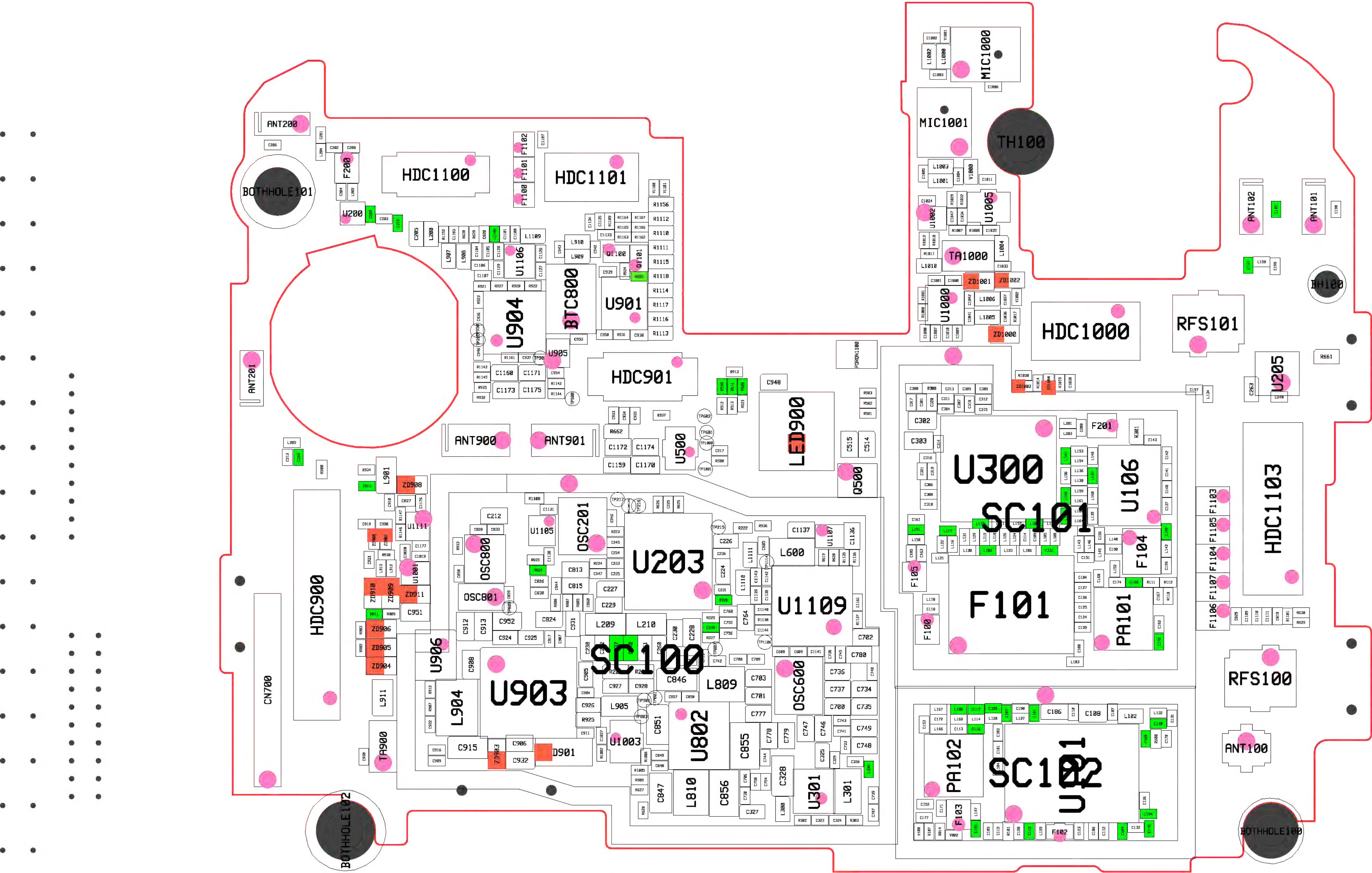

VREG_S8_1P2
CAM_HOST_1.8V
ISP_FLASH_EN
ISP_FLASH_SET
S_SPI_SSN
S_SPI_SCLK
S_SPI_MOSI
S_SPI_MISO
SENSOR_MCLK
S_SDA_1.8V
S_SCL_1.8V
SENSOR_RESET
AF_SCL
AF_SDA
CAM_SCLK
CAM_MOSI
CAM_MISO
CAM_CS
CAM_EIP_INT
SENSOR_D0_N
SENSOR_D0_P
SENSOR_D1_N
SENSOR_D1_P
SENSOR_D2_N
SENSOR_D2_P
SENSOR_D3_N
SENSOR_D3_P
SENSOR_CLK_N
SENSOR_CLK_P
13M_CAM_D0_N
13M_CAM_D0_P
13M_CAM_D1_N
13M_CAM_D1_P
13M_CAM_D2_N
13M_CAM_D2_P
13M_CAM_D3_N
13M_CAM_D3_P
13M_CAM_CLK_N
13M_CAM_CLK_P
CAM_MCLK
13M_CAM_RESET
CAM_SDA_1.8V
CAM_SCL_1.8V

MHL PART


VPH_PWR
VSIL_A_1P2_EN
VREG_S8_1P2
L1110
BLM15AG102SN1D
L1111
BLM15AG102SN1D
VCC_1.8V_MHL
VCC_3.3V_MHL
VSIL_1.2C
HD_TXC+
HD_TXC-
HD_TX0+
HD_TX0-
HD_TX1+
HD_TX1-
HD_TX2+
HD_TX2-
MHL_INT
MHL_RST
MHL_SCL_1.8V
MHL_SDA_1.8V
MHL_DM
MHL_DP
MHL_ID
HDMI_HPD

Engineer: Jack			
Drawn by: Jack			
R&D CHK:	TITLE:		SIZE: A1
DOC CTRL CHK:	J_LTE_EU		
MFG ENGR CHK:			
QA CHK:	REV	Drawing Number:	1/10

Changed by:	Date Changed:	Time Changed:
Jack	Monday, November 29, 2010	[illegible]


ANT200
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U200
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HDC1101
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Q1100
Q1101
U904
U905
HDC901
ANT201
ANT900
ANT901
C1160
C1171
C1173
C1175
C1172
C1174
C1159
C1170
U500
LED900
C948
Q500
C515
C514
MIC1000
MIC1001
TH100
U1005
U1002
TA1000
ZD1001
ZD1002
U1000
ZD1000
L1004
L1006
L1005
HDC1000
RFS101
ANT102
ANT101
BH100
U205
R661
U300
SC101
U106
F201
HDC1103
F1103
F1105
F1104
F1107
F1106
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PA101
F104
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ZD908
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ZD905
ZD904
U1111
U1001
HDC900
CN700
TA900
L911
C951
OSC800
OSC801
OSC201
U1105
U203
C212
C813
C815
C227
C229
C952
C824
L209
L210
SC100
U1107
C1137
C1136
L600
U1109
C702
C780
C736
C737
C734
C700
C735
C749
C748
OSC600
C703
C701
C777
L809
C846
U906
U903
L904
C908
C915
C906
C932
ZD903
D901
C926
R925
L905
C928
C927
U1003
C851
U802
C855
C847
L810
C856
U301
L301
C328
C327
C778
C779
C747
C746
RFS100
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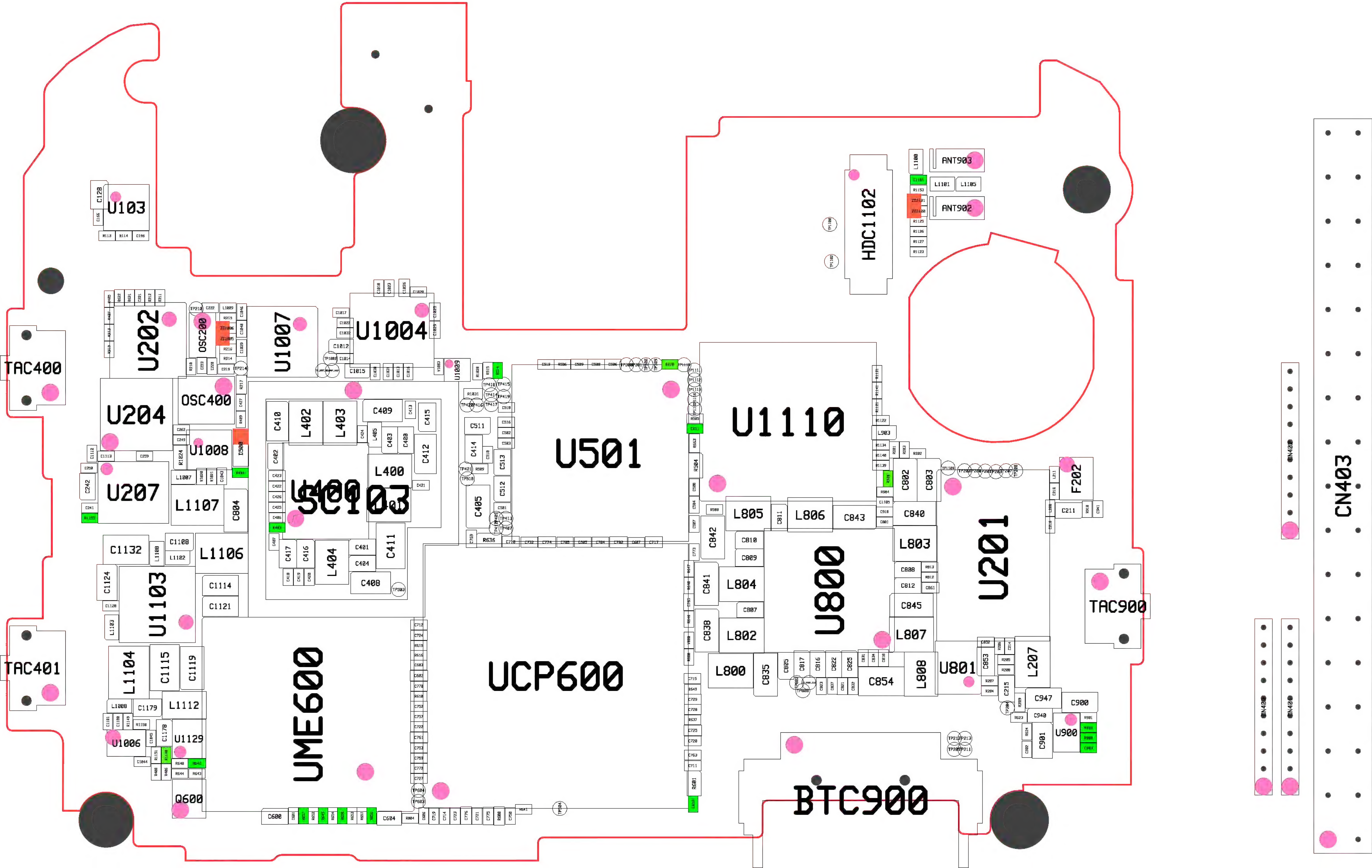
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C804
C1132
L1106
U1103
C1124
C1114
C1121
L1104
C1115
C1119
L1112
U1006
U1129
Q600
UME600
UCP600
C410
L402
L403
C409
C415
C412
L400
SC103
C411
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C511
C414
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